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APPLICATION NO.	FILING DATE	.	FIRST NAMED INVE	TOR	ATTO		RNEY DOCKET NO.		CONFIRMATION NO.	
10/614,040 07/08/2003 .		I-Ming Lin	I-Ming Lin		TOP 296			6717		
TITLE OF INVENTION: SIGNAL TESTING OF INTEGRATED CIRCUIT CHIPS										
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nonprovisional	NO	\$1400	\$300		\$0		\$1700		09/18/2007	
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BRITT, CYNTHIA H		2117	714-738000							
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VIA TECHNOLOGIES, INC. Taipei, Taiwan, R.O.C.										
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	itus (from status indicate as SMALL ENTITY state	•	☐ b. Applicant is n	o long	ger claiming SMAI	L ENT	ITY status. See 37 C	FR 1.27	(g)(2).	
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Typed or printed nam	Allen Woo	d' (·		RegistrationFil	a 1501	28,134		1400.00 OP 	
This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.										
										